	ESCRIPTION FORM	STUDY MODULE D		
42211010321631	Coc 101	5	f the module/subject	
ar /Semester	Profile of study (general academic, practical)		study	Field of
1/1	(brak)	ing	hanical Engineer	Mec
urse (compulsory, elective) obligatory	Subject offered in: Polish	Nechatronics	e path/specialty	Elective
	Form of study (full-time,part-time)		f study:	Cycle of
	full-time	Second-cycle studies		
of credits			ours	No. of h
2	Project/seminars: 1	: - Laboratory: -	re: 1 Classes	Lectur
	(university-wide, from another field)	program (Basic, major, other)	of the course in the study	Status c
	(bra	(brak)		
TS distribution (number I %)		ence and art	on areas and fields of sci	Educatio
100%		technical sciences		
2 100%		ences	Technical scie	
	d social competencies:	@put.poznan.pl rtut Elektrotechniki i Elektroniki	emysłowej 965 Poznań, ul. Piotro	ema tel. 4 Wyc Prze 60-9
nalysis	Knowledge	1		
ations on operator y to effectively self-	Skills	2		
cooperate within the	Social competencies	3		
		ectives of the course:	mptions and obi	Assu
	basic electronic components. Get a bility to design electronic circuits a	struction, parameters and uses of al electronic circuits. Acquire the a	quainted with the cons	Get ac operati
d of study	educational results for a f			
-			vledge:	Know
the construction and	c electronic components, character (_W04 + K_W07 + K_W14 +++]	operation and parameters of basi and digital electronic circuits - [/	describe principles of	1. Can
		c design criteria of electronic circu		
			3:	Skills
onic systems -	on of basic analogue and digital ele	electronics to analyze the operati	use the knowledge of 1 + K_U03 ++]	
[K_U01 ++ K_U03 +]	e electronic circuit at the basic leve	cessary for the proper design of the	specify the criteria ne	2. Can
			al competencies:	
	ectronic circuits - [K_K02 ++]	trepreneurial way in the field of el	think and act in an en	1. Can
· [K_		trepreneurial way in the field of el	al competencies:	Socia

Colloquium classifying subject.

Evaluation of the project implemented in groups.

Course descript	tion			
Update 2017.Labels (drawings, patterns, basic content) supplemented to basic electronic components and devices: passive components, semico- systems and applications. Semiconductor optoelectronic devices, their p analog circuits. Operational amplifiers - ideal and real - their properties, Analog filters - properties, design principles and their frequency charact system, logical states and logical operations, logic func- tions, truth table Applications of digital circuits. TTL systems. Semiconductor memories: types of memory. Selected simulation tools for analog and digital electron	inductor diodes, bipolar and for properties and application ex parameters, applications. Di eristics. Basic digital techniq es, digital combinational and general classification, basic of	ield transistors, and their amples. Feedback in vision. Linear systems. ues: binary number sequential circuits.		
Basic bibliography:				
1. U. Tietze, CH. Schenk, Układy półprzewodnikowe, WNT, Warszawa, 2009.				
2. P. Horowitz, W. Hill, Sztuka elektroniki. Część 1 i 2, WKŁ, 2014				
3. Z. Kulka, M. Nadachowski, Analogowe układy scalone, WKŁ, W-wa, 1980				
4. P. Górecki, Wzmacniacze operacyjne, Wydawnictwo BTC, Warszawa, 2004				
5. J. Kalisz, Podstawy elektroniki cyfrowej, WKiŁ, Warszawa, 2002				
Result of average student's workload				
Activity	Time (working hours)			
1. Participation in lectures	15			
2. Consultings of the course	2			
3. Participation in projects	15			
4. Preparing for projects	7			
Student's workl	oad			
Source of workload	hours	ECTS		
Total workload	39	2		
Contact hours	32	1		
Practical activities	22	1		